



The Hong Kong Applied Science and Technology Research Institute (ASTRI) was founded by the Government of the Hong Kong Special Administrative Region in 2000 with the mission of enhancing Hong Kong's competitiveness through applied research. ASTRI's core R&D competence in various areas is grouped under five Technology Divisions: AI and Big Data Analytics; Communications; Cybersecurity, Cryptography and Trusted Technologies; Integrated Circuits and Systems; and IoT and Sensors. It is applied across five core areas which are Smart City, Financial Technologies, Intelligent Manufacturing, Digital Health, and Application Specific Integrated Circuits.

Over the years, ASTRI has nurtured a pool of research, I&T talents and received numerous international awards for its pioneering innovations as well as outstanding business and community contributions. To date, ASTRI has transferred over 800 technologies to the industries and been granted more than 900 patents in the Mainland, the US, and other countries.

To support our constant endeavour to position Hong Kong as a world-class smart city and an international hub of innovation and technology, we are seeking qualified professionals to fill the following position(s):

Title	Embedded System Senior Lead Engineer / Lead Engineer
Job Ref No.	CTO/IOTSEN/08 (Please quote this Job Ref No. with your application.)

Job Responsibilities

- Development and system integration of embedded computer vision systems for industrial applications
- Design and evaluation based on FPGA/SoC architecture or embedded system
- Research on point cloud processing algorithms or other latest 3D vision / image processing algorithms
- Literature review and patent reading
- Preparation of technical documents

Requirement

- Ph.D. holder or Master's degree in Electronic Engineering, Computer Science or relevant disciplines with a minimum of 3 years of related experiences. Candidates with fewer experience may also be considered.
- Strong programming skills in VHDL/Verilog, C/C++ and Python, experience in cross-platform programming
- Solid understanding in FPGA programming / embedded system or SoC architecture, related firmware development, data structure, algorithms, and image processing
- Experience in analogue & digital circuit design and signal processing is a plus
- Fluent in written and spoken English

Application

The appointment will be on renewable contract terms with a competitive salary and performance-linked variable pay. Fringe benefits include paid leave, medical and dental benefits, insurance coverage and contribution to MPF. The incumbent will normally work a five-day week.

Interested candidates, please send an application (quoting Ref. No.) with a detailed resume, current and expected salary to Talent Acquisition via email to careers@astri.org. The resume should **not** include any sensitive personal information such as HKID or passport number, photo, etc.

Position offer is subject to the applicant's experience and academic qualification. The application will be open until the position is filled. Only short-listed candidates will be notified. ASTRI reserves the right not to fill the position.

ASTRI is an Equal Opportunities Employer. Personal data provided by job applicants will be used exclusively for recruitment only. For details, please refer to **ASTRI Privacy Policy Statement.**, in particular section 9.