



The Hong Kong Applied Science and Technology Research Institute (ASTRI) was founded by the Government of the Hong Kong Special Administrative Region in 2000 with the mission of enhancing Hong Kong's competitiveness through applied research. ASTRI's core R&D competence in various areas is grouped under five Technology Divisions: AI and Big Data Analytics; Communications; Cybersecurity, Cryptography and Trusted Technologies; Integrated Circuits and Systems; and IoT and Sensors. It is applied across five core areas which are Smart City, Financial Technologies, Intelligent Manufacturing, Digital Health, and Application Specific Integrated Circuits.

Over the years, ASTRI has nurtured a pool of research, I&T talents and received numerous international awards for its pioneering innovations as well as outstanding business and community contributions. To date, ASTRI has transferred over 800 technologies to the industries and been granted more than 900 patents in the Mainland, the US, and other countries.

To support our constant endeavour to position Hong Kong as a world-class smart city and an international hub of innovation and technology, we are seeking qualified professionals to fill the following position(s):

Openings: 5

Title	AI System Principal Engineer / Senior Lead Engineer / Senior Engineer
Job Ref No.	CTO/ICS/01 (Please quote this Job Ref No. with your application.)

Job Responsibilities

- Responsible for the design, modelling & simulation, implementation, verification and optimization of AI accelerator and system
- Develop leading-edge vision algorithms, FPGA/ASIC solutions, or application system software and firmware for different kinds of AI accelerators and systems used in smart city and industry 4.0
- Work with marketing and technology transfer team to prepare engineering prototypes and conduct on-site tests

Requirement

- Degree holders (Bachelor/Master/Ph.D.) in Computer Science & Engineering, Electronics Engineering, Information Engineering, or related fields with minimum of 3 years related experience
- For Software engineer: Strong programming skills in at least one of the following languages: C, C++, Python. Experience with DL frameworks such as TensorFlow, PyTorch, Caffe is a plus
- For Hardware engineer: Experience in Verilog/VHDL hardware design and FPGA/ASIC prototyping & debugging
- Good communication and interpersonal skills, a good team player
- Proficiency in written and spoken English
- Lives ASTRI values

Application

The appointment will be on renewable contract terms with a competitive salary and performance-linked variable pay. Fringe benefits include paid leave, medical and dental benefits, insurance coverage and contribution to MPF. The incumbent will normally work a five-day week

Interested candidates, please send an application (quoting Ref. No.) with a detailed resume, current and expected salary to Talent Acquisition via email to careers@astri.org. The resume should **not** include any sensitive personal information such as HKID or passport number, photo, etc.

Position offer is subject to the applicant's experience and academic qualification. The application will be open until the position is filled. Only short-listed candidates will be notified. ASTRI reserves the right not to fill the position.

ASTRI is an Equal Opportunities Employer. Personal data provided by job applicants will be used exclusively for recruitment only. For details, please refer to **ASTRI Privacy Policy Statement**, in particular section 9.