



**Hong Kong Applied Science and Technology Research Institute (ASTRI)** was founded by the Government of the Hong Kong Special Administrative Region in 2000 with the mission of enhancing HK's competitiveness in technology-based industries through applied research.

ASTRI's R&D efforts are directed towards five areas of applications, namely Smart City, Financial Technologies, Intelligent Manufacturing, Health Technologies, and Application Specific Integrated Circuits / CNERC.

Title	Associate Principal Engineer / Senior Lead Engineer, Wireless Systems Design
Job Ref No.	CTO/COM/ESYS/2643a-b/200904 (Please quote this Job Ref No. with your application.)

### Job Responsibilities

- Participate and contribute to physical layer (PHY) algorithm design and system verifications in ASTRI's 5G research and development projects. Engaged activities include
  - theoretical analysis,
  - computer aided simulation/analysis,
  - participation in design and review sessions,
  - standard compliance verification systems design and development,
  - contribution to ASTRI's patent portfolio,
  - systems support for DSP and FPGA implementation,
  - tracking and analysing evolvement of latest standard (e.g. 5G, C-V2X, O-RAN)
  - technical contributions towards proprietary or public air interface specifications.
- Experienced candidates will be managing the engineering team to achieve target design goals.

### Requirement

- Ph.D. holder or Master's degree in Electronic Engineering, Information Systems, Computer Science, Computer Engineering or relevant disciplines with a minimum of 3 years of related experiences. Candidate with less experiences may also be considered.
- Proven track record with innovative problem solving and hands-on experience in design, development, testing, and evaluation of digital wireless systems (e.g. 5G/NR, LTE, C-V2X, NB-IoT, Wi-Fi) will be an advantage.
- Extensive knowledge of radio communication theory and digital signal processing, as well as solid background in cellular communication systems across PHY, MAC and Signalling layers is preferred.
- Background in one or more of the following skills and experience:
  - Wireless physical layer (PHY) algorithm design;
  - Mobile broadband, Mission Critical, Vehicular Communications;
  - Information theory, Coding theory, Adaptive filtering, Signal Detection and Estimation, Digital Communications;
  - Modem algorithms design, including channel and interference estimation, advanced receivers, decoders, tracking loops, techniques to mitigate RF distortions;
  - Interference and mobility management in radio access networks, interference cancellation, equalization, multi-user detection;
  - OFDMA and CDMA based FDD/TDD licensed/unlicensed systems;
  - Modem implementation experience;
  - Demonstrated skills in the design of communications simulation tools.
- Good written and verbal communication and presentation skills is a plus.
- Programming/scripting skills in MATLAB, Python, C/C++ is a plus

### Application

The appointment will be on renewable contract terms with a competitive salary and performance-linked variable pay. Fringe benefits include paid leave, medical and dental benefits, insurance coverage and contribution to MPF. The incumbent will normally work a five-day week.

Interested candidates please send application (quoting Ref. No.) with detailed resume, current and expected salary to Talent Acquisition via email to [careers@astri.org](mailto:careers@astri.org)

Application will be open until the position is filled. Only short-listed candidates will be notified. ASTRI reserves the right not to fill the position.

ASTRI is an Equal Opportunities Employer. Personal data provided by job applicants will be used exclusively for recruitment only.