

HKSPIN Speaker's Meeting (28 Jan 2010)

A very interactive and interesting evening was experienced by some 30 attendees at the recent HKSPIN Speaker's Meeting. The topic which evoked such enthusiastic discussion was “Improving the Testing Process with Testing Maturity Model Integrated (TMMi®)”, delivered by Dr Hareton Leung (Hong Kong Polytechnic University) and Mr Daniel Wong.

The presentation started off with Dr Leung giving the overview of TMMi and its apparent alignment with Capability Maturity Model Integration (CMMI®). Audience who are familiar with CMMI would find it easier to grasp TMMi's concepts, practices and motivation.

TMMi evolved because some software practitioners found that CMMI does not address enough testing processes. Whereas CMMI has the backing of USA federal funds, TMMi is a non-profit foundation based in Europe.

TMMi is currently defined up to Level 3. Dr Leung invited participants to contribute in helping to refine and review the existing levels, and to define Levels 4 and 5, of which he is a member of the Working Group.

The second part of the talk was given by Mr Wong, a seasoned veteran in the software quality area, who shared real-life case studies of an existing company in Hong Kong. He explained that since the TMMi methodology was not yet ready, he employed the CMMI approach when assessing the test processes of the target company, and the user agreed on the plan and schedule.

Lively dialogues ensued even after the meeting, with many attendees eager to learn more about the “how” and implementation of TMMi within their organizations.

For those who missed this informative forum, we urge you to remember to attend the next HKSPIN Speaker's Meeting. The slides of this talk can be found on the HKCS web-site.